

PATENT FEB / 1 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Hag-ju Cho Serial No.: 09/893,035 Confirmation No.: 3421 Examiner: Erik J. Kielin Group Art Unit: 2813

Filed: June 27, 2001

METHODS OF MANUFACTURING INTEGRATED CIRCUIT DEVICES THAT INCLUDE A METAL OXIDE LAYER DISPOSED ON ANOTHER LAYER TO PROTECT THE OTHER LAYER FROM DIFFUSION OF

IMPURITIES AND INTEGRATED CIRCUIT DEVICES MANUFACTURED

USING SAME

Date: January 30, 2003

BOX RCE

Commissioner for Patents Washington, DC 20231

PETITION AND FEE FOR EXTENSION OF TIME (37 C.F.R. § 1.136(a))

- 1. This is a petition for an extension of time for a total period of one (1) month to respond to the Final Official Action mailed October 2, 2002 and Advisory Action mailed January 16, 2003.
- 2. A response in connection with the matter for which this extension is requested: is filed herewith.

is filed herewith.

has been filed.

3. Applicant

Claims small entity status. See 37 CFR 1.27.

Does not claim small entity status.

4. Calculation of extension fee (37 C.F.R. § 1.17(a)-(d)):

Total Months	Fee For Other	Fee for
Requested	Than Small Entity	Small Entity
one month	\$110.00	\$55.00
two months	\$400.00	\$200.00
three months	\$920.00	\$460.00
four months	\$1,440.00	\$720.00

Fee Enclosed: \$110.00

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110.00 GP

In re: Hag-ju Cho Serial No.: 09/893,035 Filed: June 27, 2001

Page 2 of 2

Please charge Deposit Account No. 50-0220 for any additional extension and/or fee required or credit for any excess fee paid.

Respectfully submitted,

D. Scott Moore

Registration No. 42,011



PATENT TRADEMARK OFFICE

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: BOX RCE, Commissioner for Patents, Washington, DC 20231, on January 30, 2003.

Traci A. Brown

Date of Signature: January 30, 2003